A New Test Structure for In-situ Measurements of Interface Recombination During Surface Treatments

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Abstract

In this paper a new method for to determine the recombination properties of semiconductor interfaces is presented. The recombination velocity is directly estimate by the measurement of a d.c. voltage, that develops on a special fabricate test structure, whose magnitude increases for damaged surfaces and vanishes for passivated surfaces.

Experiments carried out on two silicon samples have shown the unique feature of the method to follow step by step the quality of the surface in a complex process.

1. Introduction

The surface or interface recombination velocity S is an important parameter affecting the performances of many semiconductor devices. In spite of its importance, measurement methods usually employed for its evaluation, both optical and electrical, are not fully satisfactory. As it is well known, in fact, optical methods are mainly based on the evaluation of the decay curve of excess minority carriers, this latter is related on the cumulative effect of both bulk and surface recombination. The separation of the two contribution is a complicate task always based on fitting procedures assuming many restrictive hypothesis to be verified [1-2]. Electrical methods often use the dependence of the IV characteristics of semiconductor diodes on the surface recombination and again try to extract S by fitting experimental data with analytical models [3-4].

In this paper we present an innovative method, based on the operation of a special test device, shown in fig.1, able to directly measure the surface recombination properties without requiring the knowledge of any critical parameter.

The test device is made by a vertical diode, used to establish a chosen injection level of minority carriers into the epilayer, and by a lateral n+ region that is left floating during the operation of the vertical diode and acts as sense terminal. The d.c. voltage manifesting at the sense terminal \( V_{NN} \) only depends on the recombination properties of the surface between the diffused n+ and p+ surface region (exposed surface in Fig.1). In particular the sense voltage tends to 0 if S is very low (a passivated surface) and increases as S increases. This latter is an unique feature of the proposed method because the measure of \( V_{NN} \) gives a direct monitoring of the state of the surface without requiring post processing analysis making it suitable as an on line tool for the qualification of surface processes.

Of utmost importance is the fact that the exposed surface can be conditioned after the fabrication of the test structure so that a number of surface treatments (as RIE, amorphous or oxide deposition etc.) can be analyzed unambiguously by measuring, with the same device, the corresponding recombination velocity after each process.

In this paper the features explained above are experimentally demonstrated. The effects of surface treatments are quantified and related to process conditions.
2. Operation and design of the test structure

The measurement technique we are going to use is a modification of a test device originally developed to characterize, in term of $S$, the high-low $n^+$ transition between substrate and epitaxial layer.

In our proposal the operation of the test device is adapted to emphasize the effect of recombination on a well controlled surface.

As already stated the measurement method assumes that the vertical diode is forward biased, by the voltage $V_{PN}$, thus injecting minority carriers into the epilayer.

If the epilayer thickness $W$ is less than the minority carriers diffusion length $L_p$, the concentration of the injected minority carriers $p(0)$ can be assumed constant in the vertical direction. In the lateral direction the minority carrier concentration decreases because of the vanishing of the holes that recombine on the surface. If the sense region is placed at a distance $L$ from the $p^+$ region less than $L_p$, then the minority carrier concentration at the epilayer-sense transition $p(L)$ only depends on the surface recombination velocity and on the recombination properties of the sense-epilayer high-low transition. The carrier concentration gradient that appears in the vertical direction corresponds to a d.c. voltage manifesting at the sense terminal.

The recombination velocity associated to the high low transition can be made very low by heavily doping the $n^+$ region with respect to the epilayer. In this case $p(L)$, and hence $V_{NN}$ will depend almost entirely on the recombination velocity of the exposed surface.

A one-dimensional analytical approach to the measurement method has been presented in [5], in the following we present a two-dimensional study for the operation of the test structure of Fig.1, showing the direct relation between the surface properties and the sense voltage.

The operation of the structure has been analyzed by setting the value of the surface recombination velocity $S$ on the exposed surface and evaluating the voltage manifesting at the sense terminal when the diode in forward biased.

The results of this analysis are shown in Fig.3 for different values of $V_{PN}$. The curves clearly show the sensitivity of the sense voltage $V_{NN}$ respect to surface conditions. The curves saturate for low ($<10^{2}\text{cm/s}$) and high values of $S$. The lower value of $V_{NN}$ correspond to the recombination velocity related to the $n^+$ transition while for $S>10^6\text{cm/s}$ the sense voltage is the same as for $S$ infinite. The plots of Fig.2 are fully justified by the shape of minority carriers concentration shown in Fig.3a/b. Fig.3a shows the carrier distribution when $S$ is set to $10^2\text{cm/s}$ while Fig.3b shows the carriers distribution for $S=10^5\text{cm/s}$.

![3D carrier concentration](image)

In the first case the hole concentration gradient across the epilayer is very low; in the second case a strong gradient establishes between the epilayer-sense and the epilayer-substrate transitions giving rise to high values of $V_{NN}$. The curves obtained above depend on various geometry and physical parameters. In order to design reliable test structures their effects have been studied and simple design rules have been obtained.

![Dependence of $V_{NN}$ on the width $L$ of the exposed surface.](image)

As an example Fig.4 reports the sense voltage $V_{NN}$ as a function of $S$ having fixed the diffusion length $L_p$, for different distances $L$ of the sense region; while Fig.5 reports $V_{NN}$ for a fixed epilayer thickness $W$ and different values of $L_p$. 

![Dependence of $V_{NN}$ on $S$.](image)
The first figure shows that $V_{NN}$ is highly sensitive to the value of $S$ until $L < Lp/10$. The increase of $L$ makes $V_{NN}$ insensitive for higher values of $S$. When the distance $L$ becomes comparable with the diffusion length the univocal relationship between $V_{NN}$ and $S$ is completely destroyed.

The second figure shows the constraints that the epitaxial layer thickness has to verify. The figure shows that when the epilayer thickness $W$ is comparable with the diffusion length $Lp$ the variation of $V_{NN}$ with $S$ is very low.

The results gained from the above analysis can be summarized in two simple design rules: $L < Lp/10$ and $W < Lp/10$.

3. Experimental

The method described above has been used to characterize the fabrication process of an a-Si hetero junction. The effect of each surface treatment has been characterized by measuring the corresponding recombination properties of a specially fabricated test surface embedded in a test device having nominal physical and geometrical parameter shown in Tab. 1. The value of the recombination lifetime has been measured using the differential technique described in [6].

![Fig.5 Dependence of $V_{NN}$ on the minority carrier diffusion length](image)

The top of the realized device has been scanned by a surface profiler. A sketch of the resulting topology is shown in Fig. 6. As the surface process we are going to use are masked by aluminum, the effective exposed region is the one between the two aluminum contacts of p' and n' regions. The figure shows that it differs from the nominal exposed surface because of a slight mismatch in the mask alignment. As a consequence the thickness of the covering silicon dioxide is not uniform.

![Fig.6 Schematic of the surface profile for the realized structure](image)

<table>
<thead>
<tr>
<th>Surface treatments</th>
<th>$V_{PN}$=0.6V</th>
<th>$V_{PN}$=0.7V</th>
<th>$V_{PN}$=0.8V</th>
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</thead>
<tbody>
<tr>
<td>$V_{NN}$ (mV)</td>
<td>$S_{O2}$</td>
<td>0.63</td>
<td>2.42</td>
</tr>
<tr>
<td>$V_{NN}$ (mV)</td>
<td>$45°$ CF$_4$</td>
<td>1.04</td>
<td>5.25</td>
</tr>
<tr>
<td>$V_{NN}$ (mV)</td>
<td>$+20°$ CF$_4$</td>
<td>1.45</td>
<td>6.23</td>
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<tr>
<td>$V_{NN}$ (mV)</td>
<td>$+10°$ CF$_4$</td>
<td>1.57</td>
<td>6.47</td>
</tr>
<tr>
<td>$V_{NN}$ (mV)</td>
<td>$+10°$ CF$_4$</td>
<td>1.65</td>
<td>6.84</td>
</tr>
<tr>
<td>$V_{NN}$ (mV)</td>
<td>$H_2$</td>
<td>3.13</td>
<td>12.7</td>
</tr>
<tr>
<td>$V_{NN}$ (mV)</td>
<td>$O_2$</td>
<td>4.71</td>
<td>18.8</td>
</tr>
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</table>

The sense voltage $V_{NN}$, measured for three different values of $V_{PN}$, is shown in Tab. 2 where the results of the subsequent treatments are also reported. Our measurements show that the recombination velocity has a strong increase after this treatment. This result is not surprising because the silicon surface is no more passivated. The profiler analysis at this stage has shown that only the thinner oxide has been removed. In order to remove also the oxide still present a further 20min. CF$_4$ etch has been performed.

Table 2 Experimental results for the first sample
following 10 min. etching steps, reported in Tab. 2, only caused a further damage of the silicon surface, as highlighted by the slight increment of the sense voltage. Because the etching rate of the silicon surface resulted greater than that of the silicon dioxide, no more etching processes have been performed.

With the aim to further test the sensitivity of our method with respect to surface treatments, two other steps, hydrogenation and oxygenation, have been done. The above treatments are usually employed in p-type silicon to passivate the surfaces. Our results clearly show that, for n-type substrate, a dramatic surface damage occurs probably due to the formation of high recombinating surface states.

A second experiment has been carried out on a different device with the aim to study the silicon/amorphous-silicon interface. The results are shown in Tab. 3.

Table 3 Experimental results for the second sample

<table>
<thead>
<tr>
<th>Surface treatments</th>
<th>V_{PN}=0.6V</th>
<th>V_{PN}=0.7V</th>
<th>V_{PN}=0.8V</th>
</tr>
</thead>
<tbody>
<tr>
<td>V_{NN} (mV) S_O_2</td>
<td>1.21</td>
<td>9.82</td>
<td>46.20</td>
</tr>
<tr>
<td>V_{NN} (mV) 30' CF_4</td>
<td>7.40</td>
<td>37.98</td>
<td>93.54</td>
</tr>
<tr>
<td>V_{NN} (mV) a-S_i</td>
<td>4.53</td>
<td>22.45</td>
<td>69.74</td>
</tr>
</tbody>
</table>

Also in this case V_{NN} has been first measured with the cover oxide still present and after a CF\_4 etch. Then a-Si deposition has been performed at 400°C in a SiH\_4 atmosphere. Tab. 3 show that the a-Si deposition clearly improves the quality of the interface with respect to the un-passivated one while as it was expected the quality of the surface covered by thermal oxide is still better.

The measured sense voltages can be related to the surface recombination velocities S by fitting the experimental data with a simulated experiment using only S as parameter. The result of such procedure, applied to the first device to determine recombination velocity of the Si-SiO\_2 interface is shown in Fig. 7 where the result of a sensitivity analysis is also reported.

The circles represent the V_{NN} values measured as a function of V_{PN}, the fitting curve has been obtained by simulating the operation of a device having the same parameters of the real structure (reported in Tab. 1) and using S=3\cdot10^3 cm/s. In order to show the reliability of the fitting procedure the curves obtained for different value of S are also reported. The results show that actual value of S must be comprise between 1\cdot10^3 cm/s-6\cdot10^3 cm/s.

The determination of S has not been repeated for all the experimental data obtained along this work because of the uncertainty regarding the actual extension of the exposed silicon surface. However this fact should not be considered a serious drawback as the emphasis have to be pointed on the capability of the technique to give an immediate picture of the effectiveness of surface processes.

The extreme sensitivity of the sense voltage to the variation of the surface condition make the measurement method suitable for on-line monitoring a great variety of technological processes. The results thus obtained could also help to better understand the physic of surface recombination and can be used in process tailoring and design.

4. Acknowledgments

We take pleasure in acknowledging the competent technical work of Ivana Nasti in preparing the samples.

5. References


